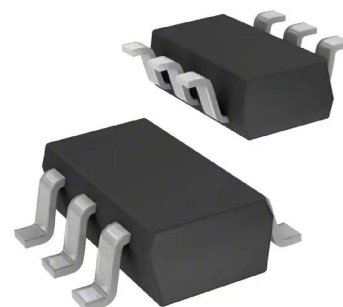


30V、1.2A、1.4MHZ Synchronous DCDC Step-down Switch-mode Converter

SSP9810

General Description

The SSP9810 is a high frequency, synchronous, rectified, step-down, switch-mode converter with internal power MOSFETs. It offers a very compact solution to achieve a 1.2A continuous output current over a wide input supply range, with excellent load and line regulation. The SSP9810 requires a minimal number of readily available, external components and is available in a space saving SOT23-6 package.



Features

- Output Adjustable from 0.8V
- Wide 4V to 30V Operating input Range
- 1.2A Continuous Output Current
- No Schottky Diode Required
- Integrated internal compensation
- 1.4MHz Frequency Operation
- Short Protection with Hiccup-Mode
- Built-in Over Current Limit
- Thermal Shutdown
- Built-in Over Voltage Protection
- Available in SOT23-6 Package
- Internal Soft start
- -40°C to +85°C Temperature Range

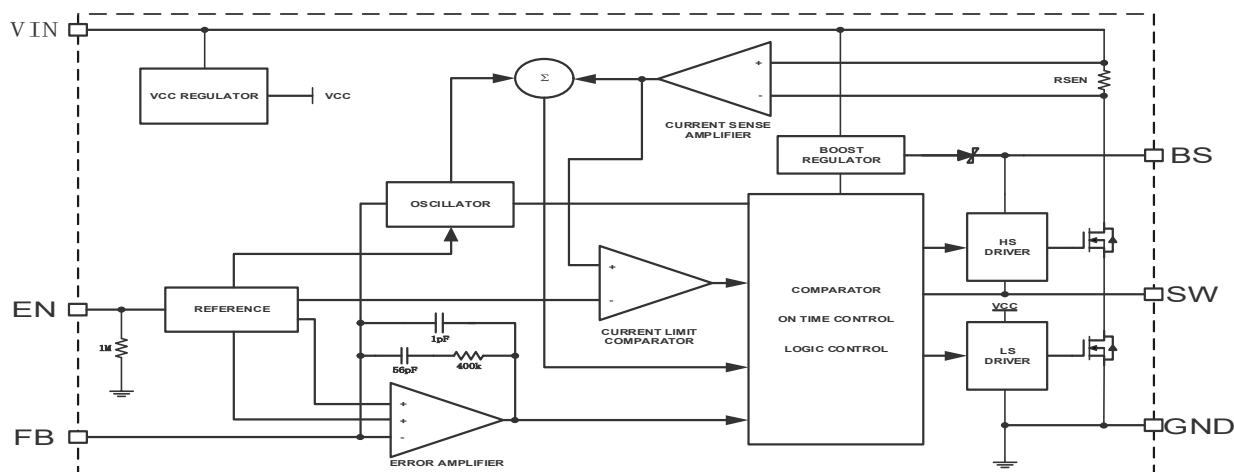
Applications

- Digital Set-top Box (STB)
- Digital Video Recorder (DVR)
- Tablet Personal Computer (Pad)
- Portable Media Player (PMP)
- Flat-Panel Television and Monitor
- General Purposes

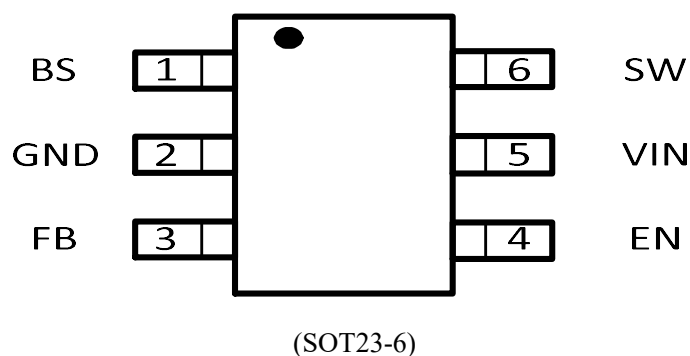
Order information

Device	Package	Packaging style	SPQ
SSP9810	SOT23-6	Reel	3000

Functional Block Diagram



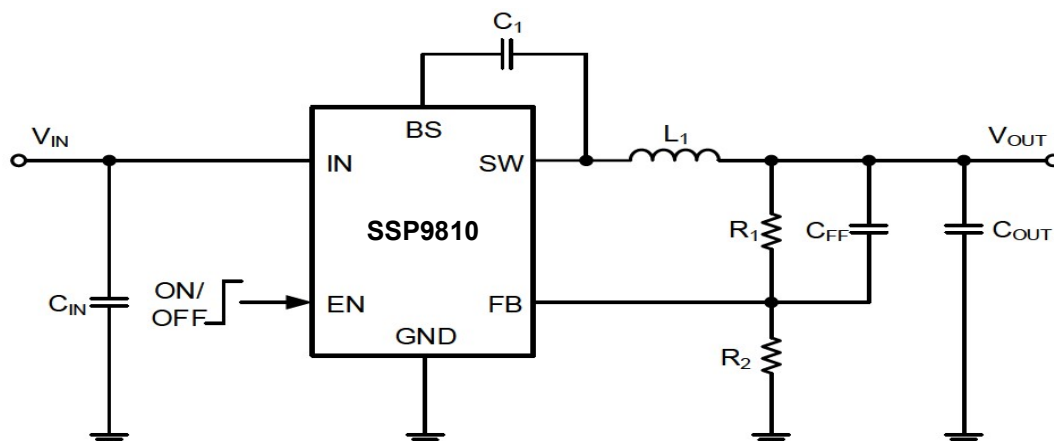
Pin Configuration



Pin Description

PIN	NAME	FUNCTION
1	BS	Bootstrap. A capacitor connected between SW and BST pins is required to form a floating supply across the high-side switch driver.
2	GND	GROUND Pin
3	FB	Adjustable Version Feedback input. Connect FB to the center point of the external resistor divider
4	EN	Drive this pin to a logic-high to enable the IC. Drive to a logic-low to disable the IC and enter micro-power shutdown mode.
5	IN	Power Supply Pin
6	SW	Switching Pin

Selection Table



Recommended reference

V _{OUT}	R1	R2	L1 _{MIN}	L1 _{TYP}	L1 _{MAX}	C _{IN}	C _{OUT}
1V	2.5KΩ	10KΩ	2.2μH	2.2μH	4.7μH	20-47uF	20-68uF
1.05V	3.13KΩ	10KΩ	2.2μH	2.2μH	4.7μH	20-47uF	20-68uF
1.2V	5KΩ	10KΩ	2.2μH	2.2μH	4.7μH	20-47uF	20-68uF
1.5V	8.75KΩ	10KΩ	2.2μH	2.2μH	4.7μH	20-47uF	20-68uF
3.3V	31.25KΩ	10KΩ	3.3μH	3.3μH	4.7μH	20-47uF	20-68uF
5.0V	52.5KΩ	10KΩ	3.3μH	4.7μH	4.7μH	20-47uF	20-68uF

Absolute Maximum Ratings

Parameter		MIN	MAX	UNIT
Input Supply Voltage	V _{in}	-0.3	36	V
FB Voltages	V _{FB}	-0.3	6	V
SW Voltage	V _{sw}	-0.3	V _{in} +0.5	V
BS Voltage	V _{BS}	V _{sw} -0.3	V _{sw} +5	V
Operating Temperature Range		-40	+85	°C
Storage Temperature Range	T _{stg}	-65	+150	°C
Lead Temperature (Soldering, 10s)		-	300	°C

Electrical Characteristics

(VIN=12V, Vout=5V, TA = 25°C, unless otherwise noted.)

Parameter	Conditions	MIN	TYP	MAX	unit
Input Voltage Range		4	-	30	V
Supply Current in Operation	VEN=3.0V, VFB=1.1V	-	0.4	0.6	mA
Supply Current in Shutdown	VEN =0 or EN = GND	-	-	4	uA
Regulated Feedback Voltage	TA = 25°C, 4V≤VIN ≤18V	0.79 1	0.807	0.823	V
High-Side Switch On-Resistance		-	200	-	mΩ
Low-Side Switch On-Resistance		-	150	-	mΩ
High-Side Switch Leakage Current	VEN=0V, VSW=12V	-	-	1	uA
Upper Switch Current Limit	Minimum Duty Cycle	-	1.2	-	A
Oscillation Frequency		1.2	1.4	1.6	MHz
Maximum Duty Cycle	VFB=0.807V	90	95	-	%
EN up Threshold	VEN-Rising	1.05	1.15	1.25	V
Soft Start		-	0.79	-	mS
Maximum Duty Cycle	VFB=0.807V	90	95	-	%
Minimum On-Time		-	60	-	nS
Minimum Off-Time		-	140	-	nS
Thermal Shutdown		-	160	-	°C
Thermal Hysteresis		-	20	-	°C

Functions Description

Internal Regulator

The SSP9810 is a current mode step down DC/DC converter that provides excellent transient response with no extra external compensation components. This device contains an internal, low resistance, high voltage power MOSFET, and operates at a high 600K operating frequency to ensure a compact, high efficiency design with excellent AC and DC performance.

Error Amplifier

The error amplifier compares the FB pin voltage with the internal FB reference (VFB) and outputs a current proportional to the difference between the two. This output current is then used to charge or discharge the internal compensation network to form the COMP voltage, which is used to control the

power MOSFET current. The optimized internal compensation network minimizes the external component counts and simplifies the control loop design.

Internal Soft-Start

The soft-start is implemented to prevent the converter output voltage from overshooting during startup. When the chip starts, the internal circuitry generates a soft-start voltage (SS) ramping up from 0V to 0.6V. When it is lower than the internal reference (REF), SS overrides REF so the error amplifier uses SS as the reference. When SS is higher than REF, REF regains control. The SS time is internally fixed to 1.2ms.

Over Current Protection & Hiccup

The SSP9810 has cycle-by-cycle over current limit when the inductor current peak value exceeds the set current limit threshold. Meanwhile, output voltage starts to drop until FB is below the Under-Voltage (UV) threshold, typically 25% below the reference. Once a UV is triggered, the SSP9810 enters hiccup mode to periodically restart the part. This protection mode is especially useful when the output is dead-short to ground. The average short circuit current is greatly reduced to alleviate the thermal issue and to protect the regulator. The SSP9810 exits the hiccup mode once the over current condition is removed.

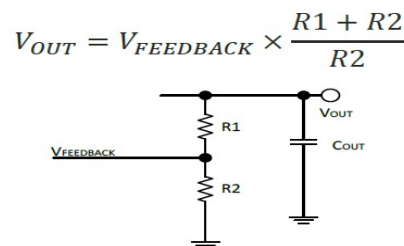
Startup and Shutdown

If both VIN and EN are higher than their appropriate thresholds, the chip starts. The reference block starts first, generating stable reference voltage and currents, and then the internal regulator is enabled. The regulator provides stable supply for the remaining circuitries. Three events can shut down the chip: EN low, VIN low and thermal shutdown. In the shutdown procedure, the signaling path is first blocked to avoid any fault triggering. The COMP voltage and the internal supply rail are then pulled down. The floating driver is not subject to this shutdown command.

Applications Information

Setting the Output Voltage

SSP9810 require an input capacitor, an output capacitor and an inductor. These components are critical to the performance of the device. SSP9810 are internally compensated and do not require external components to achieve stable operation. The output voltage can be programmed by resistor divider.



Selecting the Inductor

The recommended inductor values are shown in the Application Diagram. It is important to guarantee the inductor core does not saturate during any foreseeable operational situation. The inductor should be rated to handle the peak load current plus the ripple current: Care should be taken when reviewing the different saturation current ratings that are specified by different manufacturers. Saturation current ratings are typically specified at 25°C, so ratings at maximum ambient temperature of the application should be requested from the manufacturer.

$$L = \frac{V_{out} \times (V_{in} - V_{out})}{V_{in} \times \Delta I_L \times f_{OSC}}$$

Where ΔI_L is the inductor ripple current. Choose inductor ripple current to be approximately 30% if the maximum load current, 2A. The maximum inductor peak current is:

$$I_{L(MAX)} = I_{LOAD} + \frac{\Delta I_L}{2}$$

Under light load conditions below 100mA, larger inductance is recommended for improved efficiency.

Selecting the Output Capacitor

Special attention should be paid when selecting these components. The DC bias of these capacitors can result in a capacitance value that falls below the minimum value given in the recommended capacitor specifications table.

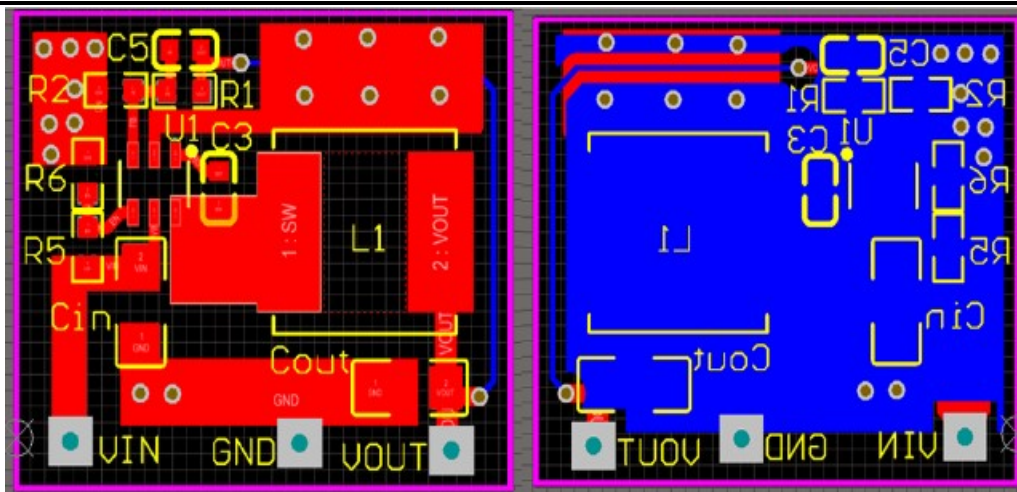
The ceramic capacitor's actual capacitance can vary with temperature. The capacitor type X7R, which operates over a temperature range of -55°C to $+125^{\circ}\text{C}$, will only vary the capacitance to within $\pm 15\%$. The capacitor type X5R has a similar tolerance over a reduced temperature range of -55°C to $+85^{\circ}\text{C}$. Many large value ceramic capacitors, larger than 1uF are manufactured with Z5U or Y5V temperature characteristics. Their capacitance can drop by more than 50% as the temperature varies from 25°C to 85°C . Therefore X5R or X7R is recommended over Z5U and Y5V in applications where the ambient temperature will change significantly above or below 25°C .

Tantalum capacitors are less desirable than ceramic for use as output capacitors because they are more expensive when comparing equivalent capacitance and voltage ratings in the 0.47uF to 44uF range. Another important consideration is that tantalum capacitors have higher ESR values than equivalent size ceramics. This means that while it may be possible to find a tantalum capacitor with an ESR value within the stable range, it would have to be larger in capacitance (which means bigger and more costly) than a ceramic capacitor with the same ESR value. It should also be noted that the ESR of a typical tantalum will increase about 2:1 as the temperature goes from 25°C down to -40°C , so some guard band must be allowed.

PC Board Layout Consideration

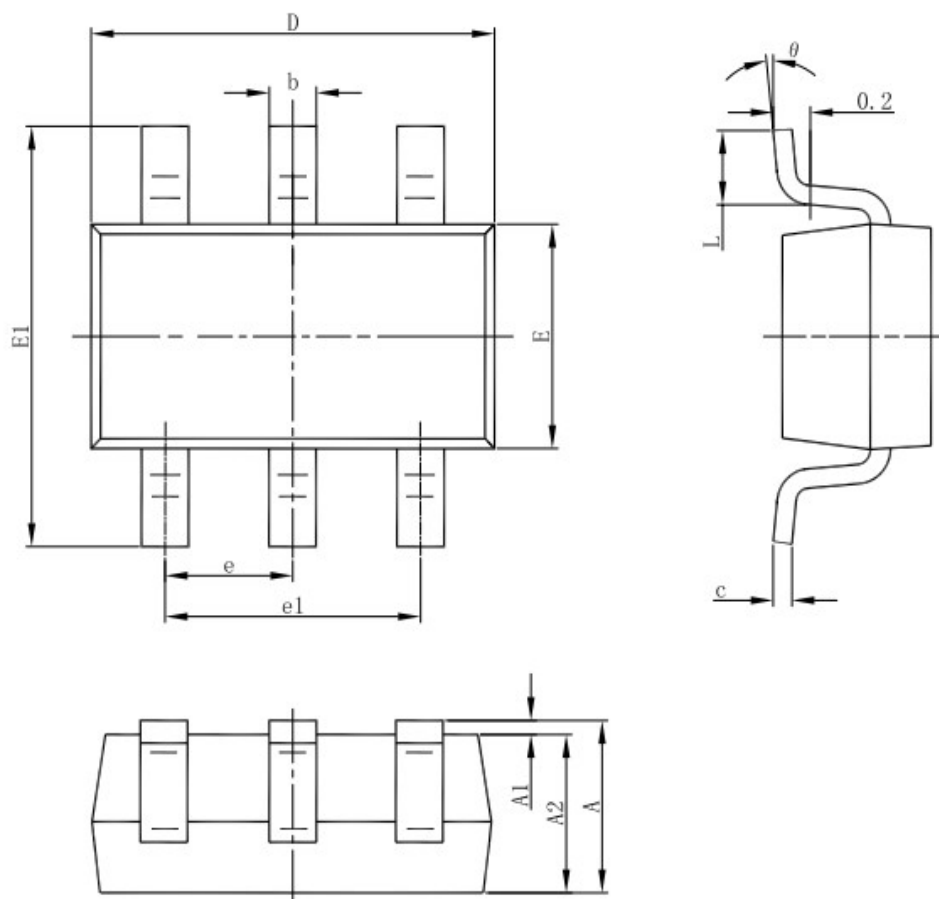
PCB layout is very important to achieve stable operation. It is highly recommended to duplicate EVB layout for optimum performance. If change is necessary, please follow these guidelines.

1. Keep the path of switching current short and minimize the loop area formed by Input capacitor, high-side MOSFET and low-side MOSFET.
2. Bypass ceramic capacitors are suggested to be put close to the Vin Pin.
3. Ensure all feedback connections are short and direct. Place the feedback resistors and compensation components as close to the chip as possible.
4. VOUT, SW away from sensitive analog areas such as FB.
5. Connect IN, SW, and especially GND respectively to a large copper area to cool the chip to improve thermal performance and long-term reliability.



Package Description

6-pin SOT23-6 Outline Dimensions



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.050	1.250	0.041	0.049
A1	0.000	0.100	0.000	0.004
A2	1.050	1.150	0.041	0.045
b	0.300	0.500	0.012	0.020
c	0.100	0.200	0.004	0.008
D	2.820	3.020	0.111	0.119
E	1.500	1.700	0.059	0.067
E1	2.650	2.950	0.104	0.116
e	0.950(BSC)		0.037(BSC)	
e1	1.800	2.000	0.071	0.079
L	0.300	0.600	0.012	0.024
θ	0°	8°	0°	8°

Special Version

The company reserves the right of final interpretation of this specification.

Version Change Description

Versions: V1.6

Writer: Xin CHun Li

Time: 2021.9.30

Amendant record:

1.Re-typesetting the manual and checking some data